

"K. J. Somaiya Institute of Engineering and Information Technology
Sion, Mumbai - 400022
NAAC Accredited Institute with 'A' Grade
NBA Accredited 3 Programs
(Computer Engineering, Electronics & Telecommunication Engineering and Electronics Engineering)
Permanently Affiliated to University of Mumbai

EXAMINATION TIME TABLE (DECEMBER 2020 /JANUARY 2021)

T.E.(COMPUTER)(Sem VI) (REV-2012) (CBSGS)

Days and Dates	Time	Course Code	Paper
Thursday, December 24, 2020	3.30 p.m. to 5.30 p.m.	CPC601	SYSTEM PROGRAMMING AND COMPILER CONSTRUCTION
Monday, December 28, 2020	3.30 p.m. to 5.30 p.m.	CPC602	SOFTWARE ENGINEERING
Wednesday, December 30, 2020	3.30 p.m. to 5.30 p.m.	CPC603	DISTRIBUTED DATABASES
Friday, January 1, 2021	3.30 p.m. to 5.30 p.m.	CPC604	MOBILE COMMUNICATION AND COMPUTING

Change if any, in the time table shall be communicated on the college web site.

Mumbai
Thursday, December 10, 2020

Principal

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EXAMINATION TIME TABLE (DECEMBER 2020 /JANUARY 2021)

T.E.(INFORMATION TECHNOLOGY)(Sem VI) (REV-2012) (CBSGS)

Days and Dates	Time	Course Code	Paper
Thursday, December 24, 2020	3.30 p.m. to 5.30 p.m.	TEITC601	SOFTWARE ENGINEERING
Monday, December 28, 2020	3.30 p.m. to 5.30 p.m.	TEITC602	DISTRIBUTED SYSTEMS
Wednesday, December 30, 2020	3.30 p.m. to 5.30 p.m.	TEITC603	SYSTEM AND WEB SECURITY
Friday, January 1, 2021	3.30 p.m. to 5.30 p.m.	TEITC604	DATA MINING AND BUSINESS INTELLIGENCE
Monday, January 04 , 2021	3.30 p.m. to 5.30 p.m.	TEITC605	ADVANCE INTERNATE TECHNOLOGY

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EXAMINATION TIME TABLE (DECEMBER 2020 /JANUARY 2021)

T.E.(ELECTRONICS & TELE-COMMN.)(Sem VI) (REV-2012) (CBSGS)

Days and Dates	Time	Course Code	Paper
Thursday, December 24, 2020	3.30 p.m. to 5.30 p.m.	ETC601	DIGITAL COMMUNICATION
Monday, December 28, 2020	3.30 p.m. to 5.30 p.m.	ETC602	DISCRETE TIME SIGNAL PROCESSING
Wednesday, December 30, 2020	3.30 p.m. to 5.30 p.m.	ETC603	COMPUTER COMMUNICATION AND TELECOM NETWORKS
Friday, January 1, 2021	3.30 p.m. to 5.30 p.m.	ETC604	TELEVISION ENGINEERING
Monday, January 04 , 2021	3.30 p.m. to 5.30 p.m.	ETC605	OPERATING SYSTEMS
Wednesday, January 06, 2021	3.30 p.m. to 5.30 p.m.	ETC606	VLSI DESIGN

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EXAMINATION TIME TABLE (DECEMBER 2020 /JANUARY 2021)

T.E.(ELECTRONICS)(Sem VI) (REV-2012) (CBSGS)

Days and Dates	Time	Course Code	Paper
Thursday, December 24, 2020	3.30 p.m. to 5.30 p.m.	EXC601	BASIC VLSI DESIGN
Monday, December 28, 2020	3.30 p.m. to 5.30 p.m.	EXC602	ADVANCED INSTRUMENTATION SYSTEMS
Wednesday, December 30, 2020	3.30 p.m. to 5.30 p.m.	EXC603	COMPUTER ORGANIZATION
Friday, January 1, 2021	3.30 p.m. to 5.30 p.m.	EXC604	POWER ELECTRONICS - I
Monday, January 04 , 2021	3.30 p.m. to 5.30 p.m.	EXC605	DIGITAL SIGNAL PROCESSING AND PROCESSORS
Wednesday, January 06, 2021	3.30 p.m. to 5.30 p.m.	EXC606	MODERN INFORMATION TECHNOLOGY FOR MANAGEMENT

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Mumbai
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